



## PATENT ABSTRACTS OF JAPAN

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(54) **PWM DRIVE CIRCUIT**

based on the Q, Q outputs of the D-FF12.

(57) Abstract:

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**PURPOSE:** To prevent surely simultaneous-on of transistors (TRs) of a power drive stage by discriminating the polarity of a drive signal with respect to a signal reference level and adopting the constitution that only the drive pulse corresponding to the drive direction is fed to the power drive stage based on the result of discrimination.

**CONSTITUTION:** A drive signal is a non-inverting input to a comparator COMP<sub>5</sub> via a resistor R<sub>15</sub>. The comparator COMP<sub>5</sub> constitutes a polarity discriminating means discriminating the polarity of the drive signal with respect to the signal reference level by using a voltage 1/2V<sub>ref</sub> as an inverting input. The discrimination output of the comparator COMP<sub>5</sub> is a data (D) input of the D-FF12. The D-FF12 used a Q output of an RS-FF4 in a triangle wave generating circuit 8 as a trigger (T) input and the Q, Q outputs are inputs to AND gates 13, 14. The AND gates 13, 14 are outputs of a AND gate 10 and a NOR gate 11, that is, the 1st and 2nd pulse signals as the other input and a gate means outputting either of the 1st and 2nd pulse signals is constituted

